



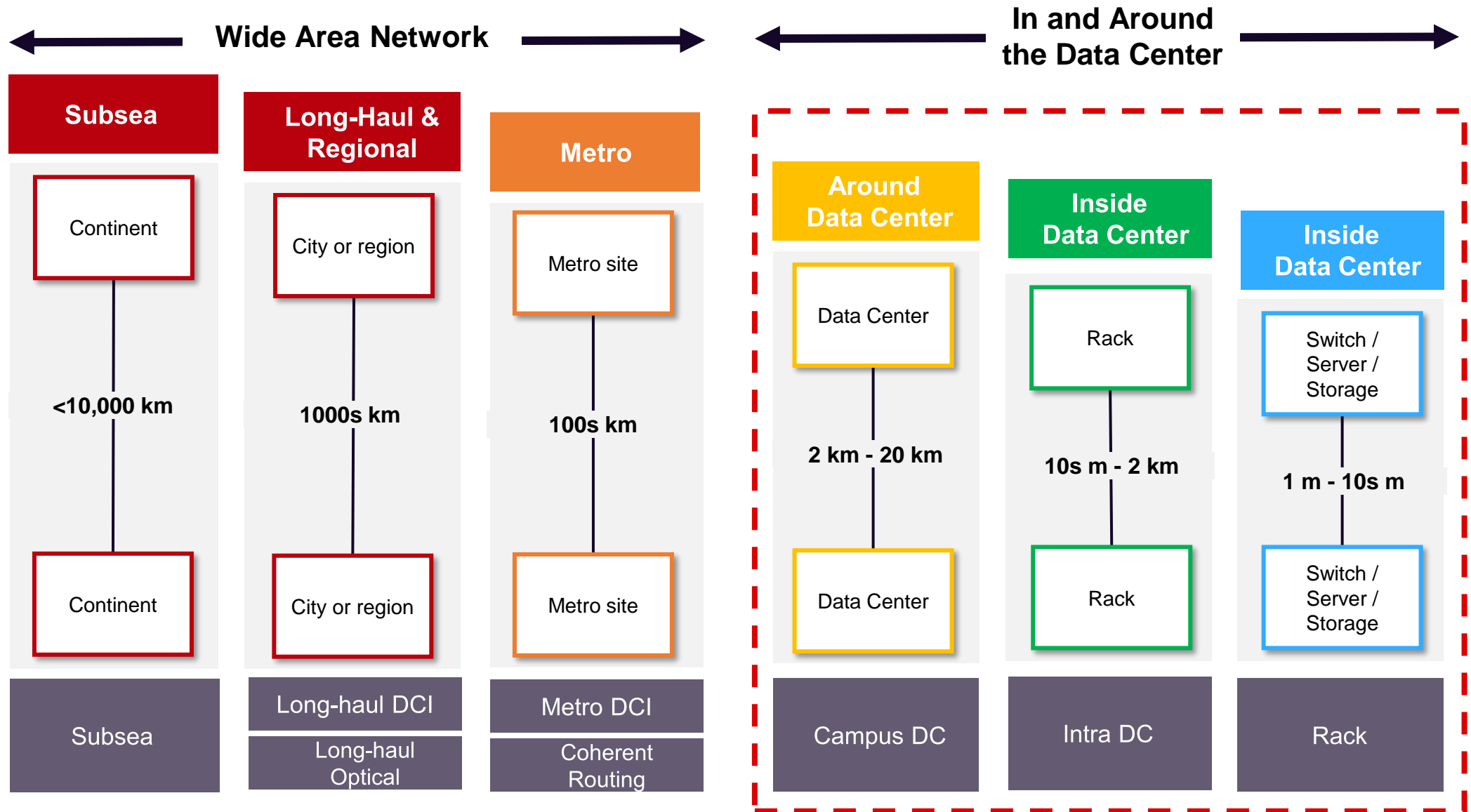
Ciena at OFC 2025

Overview of Ciena's AI and Data Center Strategy

April 1, 2025



Network applications based on reach



Coherent fits where high-bandwidth rates are required in and around the data center

		400G	800G	1.6T	3.2T
Around DC	Metro DCI <100km	Coherent	Coherent	Coherent	Coherent
	Campus <20km	IMDD	IMDD / Coherent	IMDD / Coherent	Coherent
Inside DC	Fabric <2km	IMDD	IMDD	IMDD / Coherent	IMDD / Coherent
	AI Cluster Optics/Copper <500m	IMDD	IMDD	IMDD	IMDD / Coherent

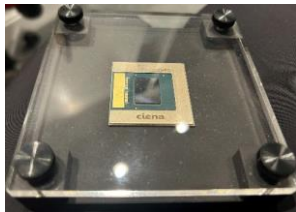
OFC demos highlight Ciena's strong vertical integration



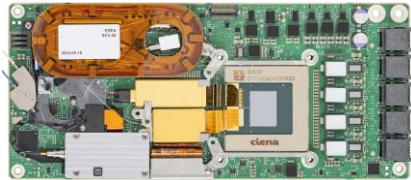
400G coherent pluggable transceivers
(Low and High Tx power)



WL6n 800G OSFP
and QSFP-DD800 pluggables

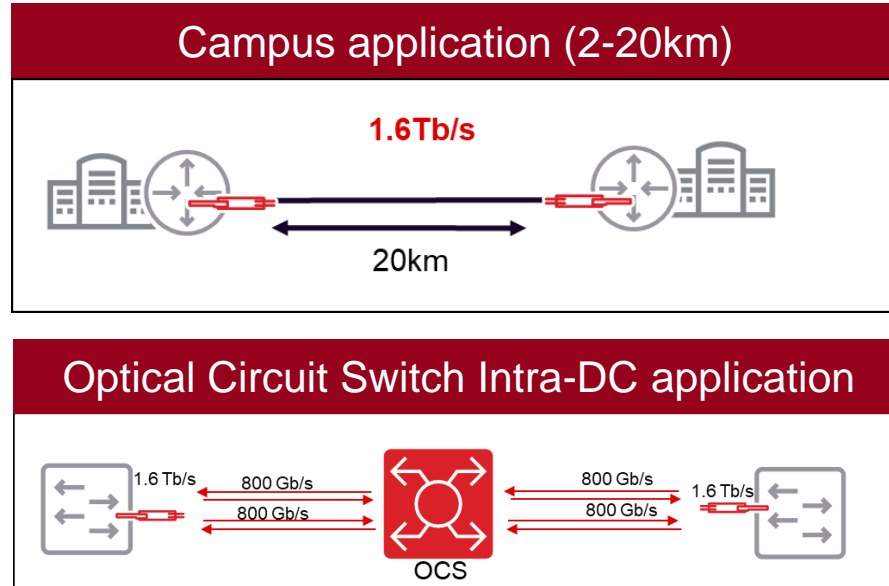


WL6e coherent DSP
ASIC



WL6e modem

1.6T Coherent-Lite brings more scale and flexibility to around-the-data-center designs



**1.6T Coherent-Lite
pluggable, based on
3nm design**

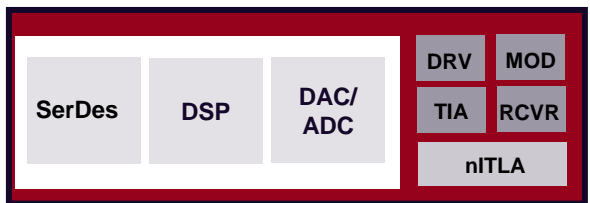
Intersects cloud/AI data center architecture requirements for higher performance at the right cost/power/latency for campus and optical circuit switch applications

Ciena is expanding to serve a broader ecosystem and range of opportunities

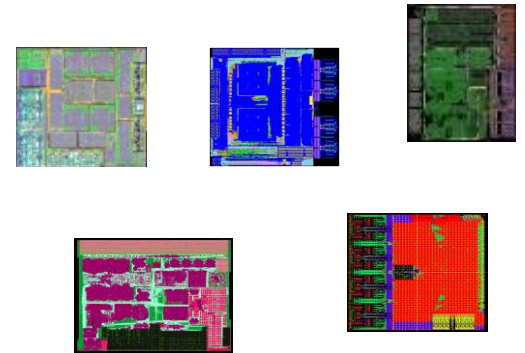
Systems



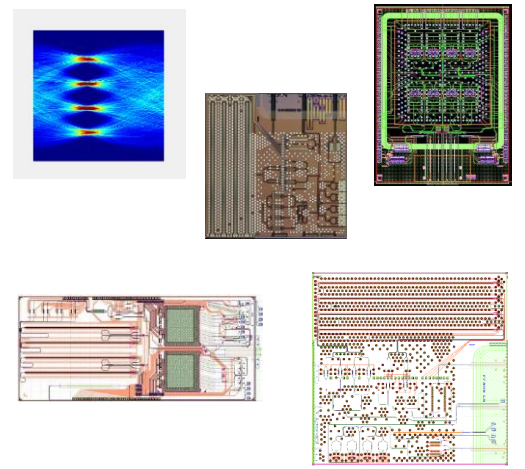
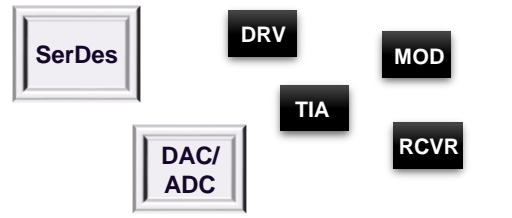
Coherent Modules



DSP ASIC



High-speed Analog and Electro-optics



TODAY


EXPANSION

SerDes – Serializer-deserializer
 DSP – digital signal processor
 DAC/ADC – digital/analog converter
 ITLA – laser assembly

DRV – driver
 RCVR – receiver
 MOD – modulator
 TIA – transimpedance amplifier

Ciena interconnects roadmap to address multiple IMDD applications

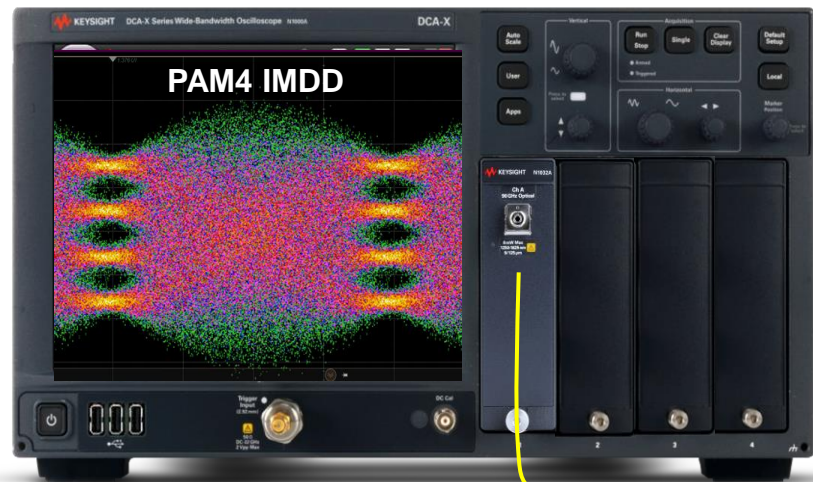
		400G	800G	1.6T	3.2T
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 High-speed interconnects for IMDD in the data center

The path to 3.2T interconnects has started

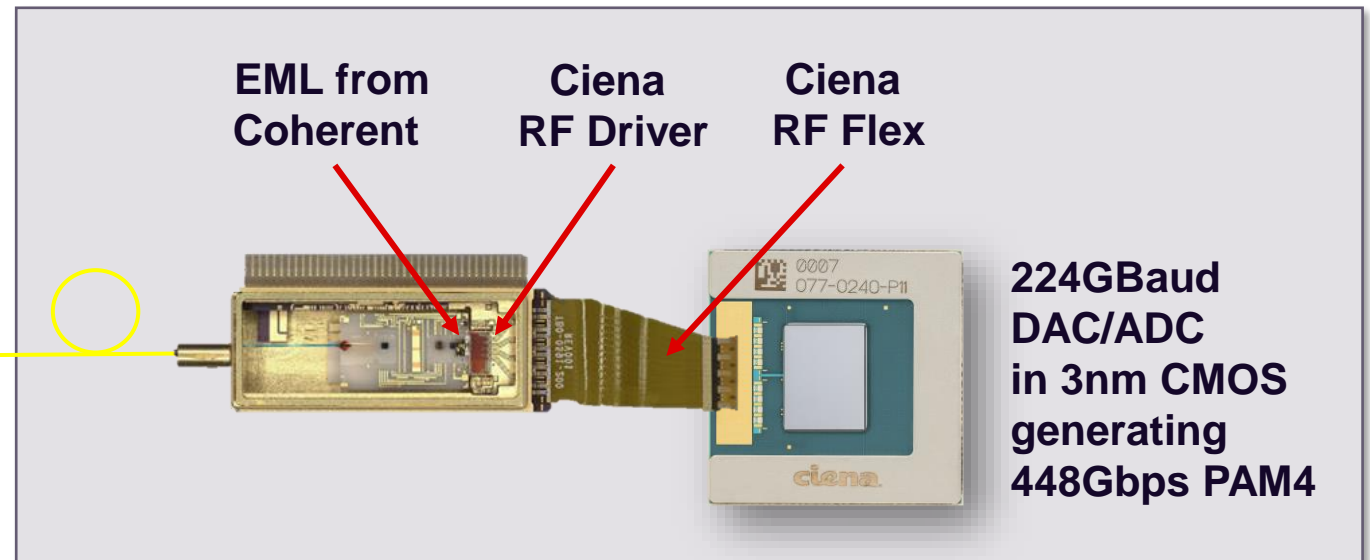
Ciena demonstrates industry-first 448Gbps per lane IMDD solution:

- 448Gb/s PAM4 working in silicon today, powered by Ciena's ultra-high-bandwidth DAC (digital-to-analog) and ADC (analog-to-digital) converters implemented in low-power 3nm CMOS
- Shows how Ciena interconnects technologies can solve critical scaling challenges in and around the data center

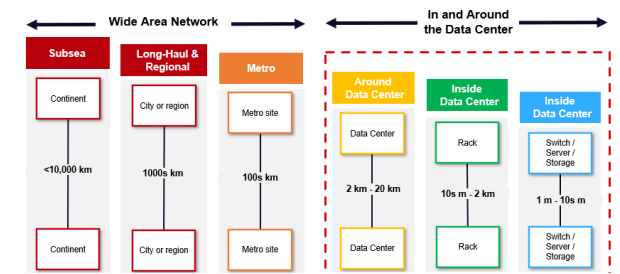


Keysight DCA-X
w/ 120GHz Optical Plug-In

Optical PAM4 signal generated by Ciena 448Gbps SerDes technology, transmitted through Ciena RF flex microstrip, amplified by Ciena driver and transmitted by EML from Coherent



Two-pronged “In and around the data center” strategy



Address the Metro DCI market with WL5 Nano (400ZR/ZR+) and WL6 Nano (800ZR/ZR+)

TODAY

COHERENT

IMDD

2026

Address the growing metro campus DC market (2-20km) (“Around the DC”) with our 1.6T Coherent-Lite solution - a variant of WL6 Nano

FUTURE

Intercept the Inside DC market with our Coherent Optical plugs and components as data rates increase to 3.2T and beyond

FUTURE

Access the Inside DC market by combining our high-speed interconnects technologies (DSP, Analog ICs) with IMDD technologies (EML, plugs, assembly)

ciena[®]

Thank You